



Professional Sound Products

# Set Up Procedure

## DJ-4111

REMOTE LEVEL CONTROL / VCA

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Refer to back page for Control Record.

Refer to **SYSTEM 41 Installation Manual** for general instructions on module installation and wiring.

The DJ-4111 is normally controlled by a 10k $\Omega$  linear potentiometer for each channel. Optionally, multiple DJ-4111 channels may be "ganged" and controlled from a single 10k $\Omega$  potentiometer.

### INPUT LEVEL CONTROLS

1. Set input level controls to "N" (Normal). Gain is 0dB, input to output (remote pot full clockwise i.e. wiper input at ground potential).
2. Full clockwise increases gain 10dB from Normal.
3. For gain structure changes, see the Module Detail for Gain Programming Resistor location, and the Gain Programming Table for resistor selection.

### REMOTE CONTROL CONNECTIONS

4. Wire 10 $\Omega$  linear potentiometer to pins 11, 12, and 13 (Channel 1) and to pins 23, 24, and 25 (Channel 2) on the 25 pin subminiature D connector as indicated in the TYPICAL CONNECTION DIAGRAM on the following page. Clockwise is full "on" (0dB attenuation).
5. With no potentiometer installed the output is at the bottom of its control range. To bypass this condition, short pins 11 to 13 (Channel 1) and /or pins 23 to 25 (Channel 2). This will turn the channel full "on".
6. An SPST switch can be placed between the potentiometer wiper and pins 11/23 (Chan 1/2) is channel muting is desired without disturbing the pot setting.
7. For control range restriction, connect a resistor R (see Connection Diagram on the following page) in series between pin 12/24 (Chan 1/2) and its respective remote potentiometer according to the formula: Range (dB) = 50dB x 10k $\Omega$ /(R + 10k $\Omega$ ). For example, if a 10k $\Omega$  resistor is added, the control range will be 25dB.
8. To simultaneously control both channels from a single remote potentiometer, move the gang jumper JH1 to position A (see Module Detail). The unused wiper pin (11 or 23), may then be used as an output to drive the wiper pin of another DJ-4111 or DJ4111-1

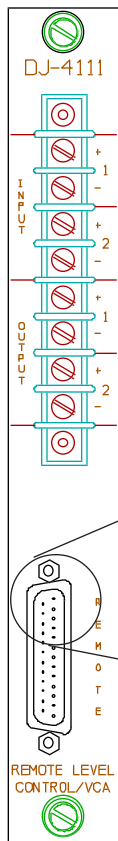
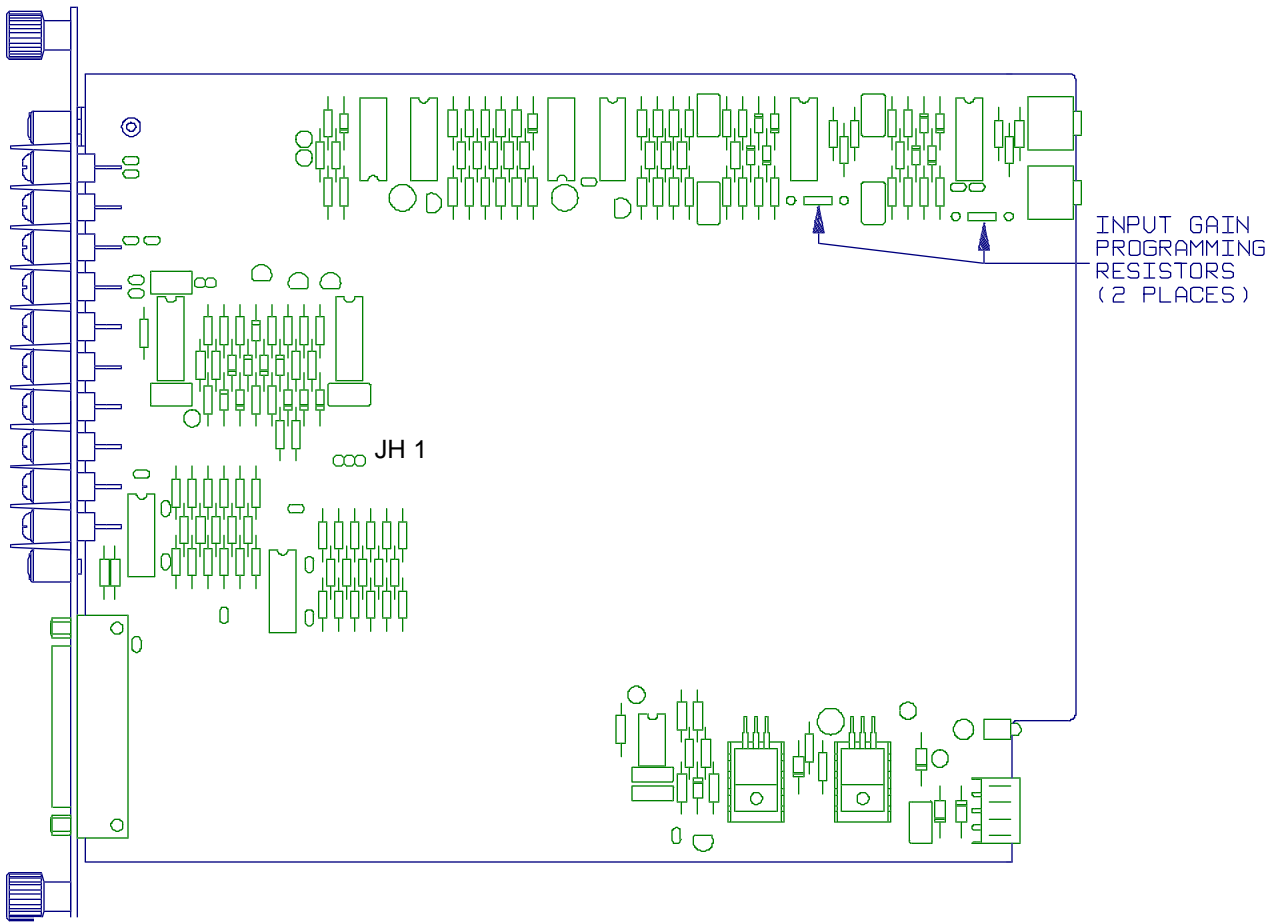
| 25 PIN SUBMINIATURE D CONNECTOR |            |     |
|---------------------------------|------------|-----|
| CH1                             |            | CH2 |
| PIN                             |            | PIN |
| 1                               | NO CONNECT | 14  |
| 2                               | NO CONNECT | 15  |
| 3                               | NO CONNECT | 16  |
| 4                               | NO CONNECT | 17  |
| 5                               | NO CONNECT | 18  |
| 6                               | NO CONNECT | 19  |
| 7                               | NO CONNECT | 20  |
| 8                               | NO CONNECT | 21  |
| 9                               | NO CONNECT | 22  |
| 10                              | NO CONNECT | -   |
| 11                              | WIPER      | 23  |
| 12                              | VCA REF.   | 24  |
| 13                              | GROUND     | 25  |



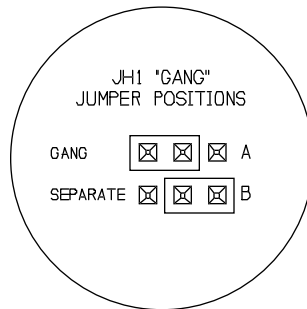
### GAIN PROGRAMMING

| GAIN PROGRAMMING RESISTOR | NOMINAL GAIN (dB) | MAXIMUM GAIN (dB) |
|---------------------------|-------------------|-------------------|
| NONE                      | -10               | 0                 |
| 12k $\Sigma$              | -5                | 5                 |
| 4.7k $\Sigma$ *           | 0                 | 10                |
| 2.2k $\Sigma$             | 5                 | 15                |
| 1k $\Sigma$               | 10                | 20                |

\* Denotes factory supplied value



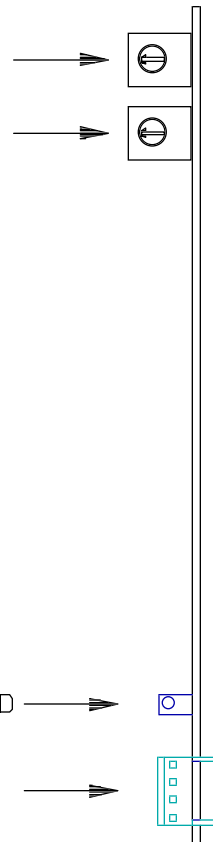
REAR PANEL



EXTERNAL INPUT CONNECTOR WIRING

INPUT LEVEL 1

INPUT LEVEL 2



FRONT EDGE CONTROLS

# Control Record

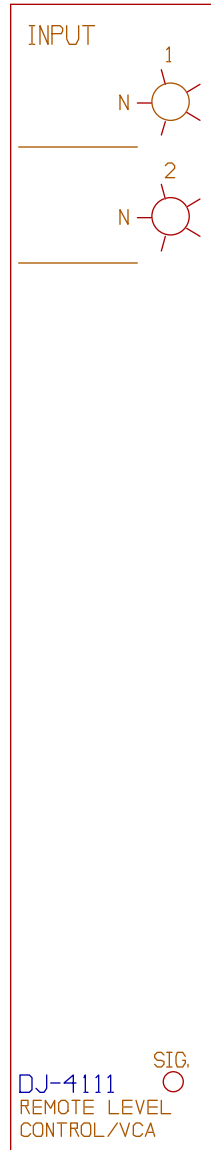
DJ-4132-2

AUDIO SIGNAL DELAY

Record all control settings on the Documentation Panel pictorial to the right. This should match the Documentation Panel in the mainframe.

Indicate the appropriate jumper positions and resistor changes in the Module Setup Table below.

| JUMPERS JH1 POSTITION                 |    |
|---------------------------------------|----|
| CHANNEL 1 RANGE RESTRICTION RESISTOR: | kΩ |
| CHANNEL 2 RANGE RESTRICTION RESISTOR: | kΩ |
| CHANNEL 1 GAIN PROGRAMMING RESISTOR:  | kΩ |
| CHANNEL 2 GAIN PROGRAMMING RESISTOR:  | kΩ |



Mainframe # \_\_\_\_\_

Module Position # \_\_\_\_\_

Contractor \_\_\_\_\_

Installer \_\_\_\_\_

Job \_\_\_\_\_

Date \_\_\_\_\_